

AUTOMATIC PERFORMANCE SYSTEM

BACKGROUND OF THE INVENTION

[0001] Field of the Invention

[0002] Embodiments of the present invention relate systems for providing automatic performance of music.

[0003] Related technology

[0004] A conventional automatic musical performance system stores a plurality of performance patterns. Each performance pattern comprises events defining musical tones and their timing, and each is typically one to two bars in length. The performance patterns are typically classified by musical genre. A composer may arrange a group of performance patterns to form a pattern sequence, and an automatic performance may be carried out by producing tones in accordance with the performance patterns of the pattern sequence. An example of such a system is provided in Japanese Unexamined Patent Application Publication (Kokai) Number 3-192299.

[0005] However, in this system it is difficult to edit the individual tones of a performance sequence. To do so, the composer or performer must rewrite an entire new performance pattern and substitute it for an existing performance pattern in the pattern sequence, even where it is desired to change only a single note. Since composition often involves trying many different variations until a desirable combination of notes is found, it is very time consuming to compose using this system.

SUMMARY OF THE INVENTION

[0006] Embodiments of the present invention improve over the aforementioned system by enabling the performer or composer to edit the patterns of notes in a pattern sequence with little effort compared to that required by the aforementioned system.

[0007] In accordance with embodiments of the invention, the user provides input during performance indicating tones to be added to or deleted from the musical performance. This causes the data representing the musical performance to be updated by adding or deleting the specified tones from the performance at the locations within the performance at which the user input for each tone was received.

[0008] In accordance with one embodiment, a device includes operators that represent various tones such as musical notes or percussive sounds. During performance of a pattern sequence, the user uses the operators to indicate notes that should be added and the points at which they are added, as well as notes to be deleted. In response to this input, a new performance pattern is created from the performance pattern of the pattern sequence that is being executed when the input is received. The new performance pattern includes the added notes and does not include the deleted notes. The pattern sequence is then updated to substitute the pattern identifier of the new performance pattern in place of the pattern identifier of the performance pattern from which it was created. Consequently the pattern sequence is updated in a simple intuitive manner.

DESCRIPTION OF THE DRAWINGS

[0009] Figure 1 is a block drawing that shows the electrical configuration of the automatic performance system of the present invention;

[0010] Figure 2 is a front elevation drawing of the operator panel of the automatic performance system of the present invention;

[0011] Figure 3(a) is a drawing that schematically shows the configuration of one set of pattern sequence data and Figure 3(b) is a drawing that schematically shows the performance pattern data memory and the configuration of one set of performance pattern data;

[0012] Figure 4 is a flowchart that shows the main processing that is executed by the CPU;

[0013] Figure 5 is a flowchart that shows the timer interrupt processing of a first preferred embodiment of the automatic performance system of the present invention;

[0014] Figure 6(a) and Figure 6(b) are drawings that schematically show the changes in the pattern sequence data in accordance with the first preferred embodiment of the automatic performance system of the present invention; Figure 6(a) shows the pattern sequence data before the change and Figure 6(b) shows the pattern sequence data after a partial change;

[0015] Figure 7 is a flowchart that shows the timer interrupt processing in accordance with a second preferred embodiment of the automatic performance system of the present invention;

[0016] Figure 8 is a flowchart that shows the performance processing in the second preferred embodiment of the present invention; and

[0017] Figure 9(a) and Figure 9(b) are drawings that schematically show the changes in the pattern sequence data in accordance with a third preferred embodiment of the present invention; Figure 9(a) shows the pattern sequence data before the change and Figure 9(b) shows the pattern sequence data after a partial change.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0018] An explanation will be given below regarding a desirable preferred embodiment of the present invention while referring to the attached drawings. Figure 1 is a block drawing that shows hardware components of an automatic performance system 1 in accordance with a first preferred embodiment of the present invention.

[0019] The automatic performance system 1 is primarily furnished with the CPU 10 as the central processing unit with which the overall control of the automatic performance system is done, a ROM 14 in which the control program that controls the CPU 10 and the various types of data tables, such as the preset

performance pattern table and the like, are stored, a RAM 16 that has a working area in which the various types of registers that are required by the control program that is executed by the CPU 10 and the like are set and an area in which pattern sequence data that have been created by the operator (the performer or the composer) and performance pattern data that have been read out from the ROM 14 are stored, and a temporary area in which data that are being processed are temporarily stored, a group 18 of various types of operators for the operation of the functions and effects of the automatic performance system 1, a liquid crystal display system (the display section) 20 with which the various types of information concerning the performance that is executed by the automatic performance system 1 are displayed, a sound source section 22 with which the musical tones of the performance that is executed by the automatic performance system 1 are produced, and a bus 12 as the path for the connection and exchange of data among each of these various structures.

[0020] Next, an explanation will be given regarding the operating panel portion on which the operator group 18 is disposed while referring to Figure 2. Figure 2 is a drawing that shows the operator panel of the automatic performance system 1 of the present invention.

[0021] The automatic performance system 1 includes a liquid crystal display section 20 that is formed in a roughly rectangular box shape viewed from the front and that displays various types of information concerning the automatic performance system, an automatic performance mode switch 2 for selecting the modes of the automatic performance, a pad mode switch 3 for selecting the modes of the pad, the eight pad (PAD) switches 4 for selecting performance patterns or musical tones, a start/pause (START) switch 5 for starting or pausing the automatic performance, a stop (STOP) switch 6 for stopping the automatic performance, a step recording (STEP REC) switch 7 for switching to the step recording mode, a delete (DEL) switch 8 that instructs the cancellation of the events (the musical tones, volume and the like) that are contained in performance

patterns, a tempo (TEMPO) switch 9 for instructing the changes in the tempo of the song, and a rotary encoder 10 for adjusting each of the various parameters for the execution of the automatic performance. Although it is not included in the operator panel of Figure 2, a pedal switch that is operated with the foot and the like may be included in the operator group. In addition, in the operator panel that is shown in Figure 2, button switches or rotary encoders are used, but there is no particular restriction to their form and any form switch desired may be used. For example, the button switches 2 to 9 may also be in the form of knobbed switches and the like. Incidentally, with regard to the operator panel section that has been shown in Figure 2, it is not limited to the example shown of an arrangement of the operator group and the display section 20 and the arrangement can be changed as required.

[0022] The automatic performance mode switch 2 comprises a style mode (STYLE) switch 2a and a song mode (SONG) switch 2b. By pressing either one or the other of the switches, it is possible to set the mode of the automatic performance to either the style mode or the song mode.

[0023] In the automatic performance using the style mode, the performer selects a style (jazz, rock, pop and the like), and the performance is started from an intro pattern from among the performance patterns (intro pattern, main pattern and the like) that is defined for the selected style. For each style, one type of intro pattern, three types of main pattern, three types of fill pattern, and one type of end pattern are defined and assigned to a respective one of the PAD switches 4.

[0024] On the other hand, in the automatic performance using the style mode, a pattern sequence that has been selected by the performer is performed automatically. The pattern sequence is a sequence of performance patterns. As discussed later, the pattern sequence is created by the operation of the step recording switch 7 and stored as pattern sequence data in the RAM 16 (refer to Figure 3(a)). The further details that are presented herein relate to performance in song mode as opposed to style mode.

[0025] The pad mode switch 3 comprises the instrument mode (INST) switch 3a with which an event that instructs the production of a musical tone is generated and the pattern mode (PATN) switch 3b with which an event that instructs a pattern performance is generated. Because of this, by pressing either one of the INST switch 3a or the PATN switch 3b, the mode of the PAD switch 4 can be switched. In those cases where the INST switch 3a has been pressed, the kick drum (KICK) 4a, the snare 1 (SNR 1) 4b, the snare 2 (SNR 2) 4c, the open high hat (OHH) 4d, the closed high hat (CHH) 4e, the stick sound (STK) 4f, the ride cymbal (RIDE) 4g, and the crash cymbal (SYM) 4h are respectively assigned to the eight PAD switches 4. On the other hand, in those cases where the PATN switch 3b has been pressed, the intro pattern (INTRO) 4a, the fill pattern 1 (FILL 1) 4b, the main pattern 1 (MAIN 1) 4c, the fill pattern 2 (FILL 2) 4d, the main pattern 2 (MAIN 2) 4e, the fill pattern 3 (FILL 3) 4f, the main pattern 3 (MAIN 3) 4g, and the ending pattern (ENDING) 4h that have been set in advance for each style are assigned to the eight PAD switches 4 in conformance with the currently selected style.

[0026] The start (START) switch 5 is a switch with which the performance in the mode that has been selected by the automatic performance mode switch 2 is started. In those cases where the song mode switch 2b is pressed, the performance of the pattern sequence that has been selected by the performer is started. When the start switch 5 also doubles as the pause button and the start switch 5 is pressed once more during the performance, the performance is paused. After this, when the start switch is pressed again, the pause is canceled and the performance is started again. In addition, the stop (STOP) switch 6 is a switch for instructing the stopping of the performance.

[0027] The step recording switch 7 is a switch that is pressed to produce a pattern sequence by means of step recording. Here, a explanation will be given regarding the pattern sequence creation method using step recording. In the song mode (the song mode switch 2b pressed) and the pattern mode (the pattern mode

switch 3b pressed) state, the step recording switch 7 is pressed. Next, the PAD switch 4 or the rotary encoder 10 is operated and a pattern sequence is produced by the selection of performance patterns in a desired order. For example, the pattern sequence data of Figure 6(a) are produced by the selection of performance patterns in the following order: pattern number (PTN) 2-1 three times, PTN 2-2, PTN 3-1 twice, then PTN 3-2, and so on.

[0028] By pressing the delete (DEL) switch 8 during the performance of a pattern sequence that has been created, an event is generated that instructs the deletion of the performance data that corresponds to the PAD switch 4 (4a to 4h) that is pressed at the same time. When the delete switch 8 and a PAD switch 4a to 4h are pressed at the same time, only the musical tone that is assigned to the PAD switch 4a through 4h is deleted from the data. For example, in those cases where the delete switch 8 and the open high hat 4d have been pressed at the same time, the open high hat sound is deleted from the data during the time that they are pressed. On the other hand, in those cases where only the delete switch 8 is pressed independently during the performance of the song, none of the data are deleted.

[0029] The tempo (TEMPO) switch 9 is pressed to change the tempo of the musical composition during the song or the style performance. When the tempo switch 9 is pressed, the tempo value is displayed on the display section 20, and it is possible to change the tempo value by turning the rotary encoder 10.

[0030] The rotary encoder 10 is operated to change parameter values. As discussed above, the tempo can be changed in those cases where the tempo switch 9 has been pressed. The rotary encoder 10 may also be used to select a performance pattern at the time of pattern sequence creation and also to select a pattern sequence to be performed.

[0031] The liquid crystal display section 20 displays various types of information related to the song or the style. The song or style name 20a, the pattern number 20b of the performance pattern, the information 20c related to the

number of bars and the beat count of the pattern sequence that is being performed, and various types of parameters (for example, the tempo value) 20d are displayed on the display section 20.

[0032] Figure 3 is a drawing that schematically shows the configuration of pattern sequence data and performance pattern data that are stored in the automatic performance system 1. Figure 3(a) is a schematic drawing of pattern sequence data 30 that is stored in the RAM 16 and Figure 3(b) is a schematic drawing of a set of performance pattern data 40 for a style that are read out from the ROM 14 and stored in the RAM 16 and one set of performance pattern data 40b that are comprised by the performance pattern data 40.

[0033] As is shown in Figure 3(a), the pattern sequence data 30 includes a header 30a, which is a block that stores the title of the song, the tempo and the like, a block group 30b, in which the order of performance patterns in the performance sequence is represented by the stored order of respective performance pattern identifiers, and the end 30c, in which the end data that instruct the ending of the pattern sequence are stored.

[0034] As is shown in Figure 3(b), the set of performance patterns 40 for a style is comprised of a plurality 40a of individual performance patterns. Each individual performance pattern (for example, PTN 1-1 (40b)) comprises a header 40c in which the pattern name (the pattern number and identifier) is stored, a block group 40g comprised of an ordered set of event information such as a note number 40e, a velocity 40f and the like, and a time 40d for the generation of that event, and an end 40h, in which the end data that instruct the ending of the performance pattern are stored. The intro, ending, main 1 to 3, and fill 1 to 3, for a total of eight performance patterns for each of the individual styles 1 through M are stored in the set 40 of performance pattern data. Incidentally, performance patterns that have the new pattern numbers (identifiers) that are created in accordance with this preferred embodiment and the performance patterns that have been created as desired by the operator can additionally be stored in the set 40 of performance

pattern data.

[0035] Next, an explanation will be given regarding the processing that is executed in the first preferred embodiment of the automatic performance system 1 that has been configured as described above while referring to the flowcharts of Figures 4 and 5 and to Figure 6. Figure 4 is a flowchart of the main processing that is executed by the automatic performance system and this is repeatedly executed by the CPU during the time that the power is on.

[0036] First, whether or not the automatic performance system is set to carry out an automatic performance is ascertained (S1). Here, set to carry out an automatic performance means that the automatic performance mode switch 2, in other words, either the style mode switch 2a or the song mode switch 2b, is in a pressed state.

[0037] If the result that has been ascertained by the S1 processing is that the automatic performance system is set to carry out an automatic performance (S1: yes), then next, a determination is made as to whether or not the start switch 5 has been pressed (S2). If the result that has been ascertained by the S2 processing is that the start switch has been pressed (S2: yes), then the setting of the timer interrupt is carried out (S3). By means of the S3 processing, the timing interrupt processing for carrying out such processing as the reading out of the performance patterns and the output to the sound source and the like is done and the tempo value (the number of quarter notes per minute) that is set is interrupted (the timer interrupt) at the time interval (the tick) that has been divided at a specified value (for example, 120).

[0038] When, in the timer interrupt processing, it is ascertained by the S4 processing that the stop switch 6 has been pressed (S4: yes), the interrupt is prohibited by the timer interrupt prohibition processing (S5). Next, after the other processing including the setting of the tempo value and the setting of the song or the style and the like has been carried out (S6), the routine returns to S1 and the main processing is repeated.

[0039] On the other hand, in those cases where the result that has been ascertained by the S2 processing is that the start switch 5 is not pressed (S2: no), (1) the automatic performance is not started or (2) since the system is in a state in which although it is in the midst of an automatic performance, the timer interrupt setting processing (S3) discussed above is being carried out, the S3 processing is skipped and the routine moves to the S4 processing.

[0040] In addition, in those cases where the result that has been ascertained by the S4 processing is that the stop switch 6 has not been pressed (S4:NO), since either (1) the system is in the midst of automatic performance or (2) the start switch 5 has not been pressed and the system is in a performance standby, the S5 processing is skipped, the routine moves to the S6 processing, returns to S1 after that and the main processing is repeated.

[0041] Furthermore, in those cases where the result that has been ascertained by the S1 processing is that the system has not been set to carry out an automatic performance (S1: no), the routine moves to the S6 processing, returns to S1 after that and the main processing is repeated.

[0042] Next, an explanation will be given regarding the editing processing of the pattern sequence that is executed in this preferred embodiment while referring to Figure 5. Figure 5 is a flowchart of the routine that is launched by the timer interrupt processing when the start switch 5 is pressed in a state in which the song mode has been selected by the automatic performance mode switch 2 and, moreover, the instrument mode has been selected by the pad mode switch 3. In this processing, at the same time as the automatic performance based on the pattern sequence is carried out, a performance event can be added by the operation of a PAD switch 4 (4a to 4h) and, in addition, it is possible to delete performance data by the operation of the DEL switch 8 and a PAD switch 4.

[0043] In the timer interrupt processing, first, whether or not the pattern sequence has been started, in other words, whether or not the first performance pattern in the pattern sequence has been started is ascertained (S11). If the result

that has been ascertained by the processing of S11 is that the pattern sequence has been started (S11: yes), the tick value is made "0" (S12) and whether or not there are performance data for the current tick (in this case $t = 0$) of the current performance pattern (in this case, in other words, the first performance pattern) is ascertained (S18).

[0044] On the other hand, in those cases where the result that has been ascertained by the S11 processing is that the pattern sequence has not been started (S11: no), "1" is added to the tick value (t) the readout advances to the next tick (S13) and, after the processing of S13, whether or not the tick that has been advanced to next is the end of the current performance pattern is ascertained (S14). If the next tick is not the end of the performance pattern (S14: no), the routine moves to the processing of S18.

[0045] In addition, if the result that has been ascertained by the S14 processing is that it is the end of the current performance pattern, then whether or not it is the end of the pattern sequence is ascertained next (S15). If the result that is ascertained by the S15 processing is that it is not the end of the pattern sequence (S15: no), the tick value is made "0" (S16), the performance pattern is updated to the next pattern number (S17), and the routine moves to the processing of S18.

[0046] Next, if the result that is ascertained in the S18 processing as to whether or not there are performance data for the current tick of the current performance pattern is that there are performance data (S18: yes), those data are output to the sound source section 22 (S19). If data are output to the sound source by the S19 processing, next, whether any of the PAD switches 4 (4a to 4h) has been pressed is ascertained (S20). On the other hand, if the result that has been ascertained by the S18 processing is that there are no performance data (S18: no), in other words, a case in which no musical tone data exist that should be output to the sound source section 22, the processing of S19 is skipped and the routine moves to the S20 processing.

[0047] In those cases where in the S20 processing, it has been ascertained that any of the PAD switches 4 has been pressed (S20: yes), information including the time, the type of PAD switch 4 that has been pressed and the pressing strength, as well as whether or not the DEL switch 8 has been pressed at the same time, and identifier of the current performance pattern and its location within the pattern sequence, are stored in a new area of the RAM 16 (S21). After the processing of S21, the timer interrupt processing finishes and the routine returns. In addition, if the result that has been ascertained in the S20 processing is that a PAD switch 4 has not been pressed (S20: no), the timer interrupt ends as it is and returns.

[0048] On the other hand, if the result that has been ascertained by the S15 processing is that it is the end of the pattern sequence (S15: yes), a new performance pattern is produced based on the information that has been stored in the RAM 16 by the S21 processing (S22). In the case where in S22, the performance pattern that corresponds to the pattern number of the performance pattern that has been stored in the RAM 16 by the processing of S21 is read out and a PAD switch 4 (4a to 4h) has been pressed but the DEL switch 8 has not been pressed, event data that correspond to the musical tone that matches that PAD switch, the strength and the time of the tone are inserted into the performance pattern to produce a new performance pattern. On the other hand, in those cases where both a PAD switch 4 and the DEL switch 8 have been pressed at the same time, the event data that correspond to the note and time of the pressed PAD switch (4a to 4h) are deleted from the performance pattern to produce a new performance pattern. A new pattern identifier is associated with to the new performance pattern, and the pattern sequence is updated to substitute the new performance pattern identifier in place of the performance pattern that was changed by the performer's use of the PAD and DEL switches. The S22 processing is carried out for all of the information that has been stored in the RAM 16 by the processing of S21. After the S22 processing, the automatic performance

ends (S23), the timer interrupt processing finishes and returns.

[0049] Incidentally, with the timer interrupt processing of this preferred embodiment, the S20 to S21 processing is carried out after the S18 to S19 processing but the S18 to S19 processing may also be carried out after the S20 to S21 processing. In other words, even in those cases where a PAD switch 4 and the DEL switch 8 have been pressed at the same time in the S21 processing, a musical tone is generated due to the S19 processing which occurred first but it may be set up such that in those cases where the S18 to S19 processing is carried out after the S20 to S21 processing and a PAD switch 4 (4a to 4h) and the DEL switch 8 have been pressed at the same time, the musical tone that corresponds to the PAD switch 4 (4a to 4h) that has been pressed is not generated.

[0050] In addition, with the timer interrupt processing of this preferred embodiment, the information concerning the PAD switch 4 or the DEL switch 8 that were pressed in S20 is stored in a new area of the RAM 16 in the S21 processing, and, when the pattern sequence ends, a new performance pattern is produced in the S22 processing from the performance pattern at the time that the information was stored in the RAM 16 by the S21 processing and, together with this, the identifier of the pattern to which it corresponds in the pattern sequence is updated. However, it may also be set up such that this is stored to the RAM 16 together with the time data (timing data) during the performance based on the sequence pattern without differentiating the event that is produced based on the sequence pattern and the event that is produced by the operator, new identifiers are set for the performance patterns of the locations for which the operator has been operated, and the new identifiers rewritten for the locations in the pattern sequence. In this case, the operator is operated and the new identifier is maintained for the performance pattern that has been set, and the performance pattern in which the operator has not been operated is handled as an empty region.

[0051] Figure 6 is a drawing that schematically shows the changes in the pattern sequence data in accordance with the first preferred embodiment of the

automatic performance system 1 of the present invention. Figure 6(a) shows the pattern sequence data 100 before the change, and Figure 6(b) shows the pattern sequence data 110 after a partial change has been effected by an operation during the pattern sequence performance. An explanation will be given regarding this preferred embodiment while referring to Figure 6. In the automatic performance system 1 in which a performance is carried out in accordance with a pattern sequence, when the PAD switch 4 and, as required, the DEL switch 8 are operated at the desired location during the performance of PTN 2-1 (100c), corresponding portions of the performance pattern are changed by the processing of S21 and S22 discussed above, and the original performance pattern identifier PTN 2-1 (100c) in the pattern sequence is updated to the new performance pattern identifier PTN (2-1)' (110c). In the same manner, when the PAD switch 4 and, as required, the DEL switch 8 are operated during the performance of PTN 3-2 (100g), PTN 3-2 (100g) is updated in the pattern sequence to a new performance pattern having the identifier PTN (3-2)' (110g). As a result, a new pattern sequence 110 comprised of new performance patterns is obtained.

[0052] With the automatic performance system 1 of the present invention, as has been explained above, it is possible for the operator (the performer or the composer) to edit the performance patterns of a pattern sequence by pressing the PAD switch 4 and, as required, the DEL switch 8 during a performance of the pattern sequence.

[0053] Next, an explanation will be given regarding the action of a second preferred embodiment of the automatic performance system 1 of the present invention while referring to the flowcharts of Figure 7 and Figure 8. With the first preferred embodiment, when an operation is carried out to change the pattern during a pattern sequence performance, only that operation information is stored and the pattern for which there was an operation is changed after the completion of the pattern sequence performance and new pattern sequence data are produced. In contrast, in the second preferred embodiment, the performance pattern is

changed for each operation that is carried out during the pattern sequence performance. Incidentally, the same codes are appended for the portions that are identical to those in the preferred embodiment 1 and their explanation has been omitted.

[0054] Figure 7 is a flowchart of the timer interrupt processing for carrying out the pattern sequence editing processing that is executed by the second preferred embodiment. With the timer interrupt processing of the second preferred embodiment, in the same manner as in the case of the first preferred embodiment, when the start switch 5 is pressed in a state in which the song mode is selected by the automatic performance mode switch 2 and, moreover, the instrument mode is selected by the pad mode switch 3, the routine is launched for each tick in conformance with the tempo that is set.

[0055] When the timer interrupt processing is launched, first, whether or not the pattern sequence has been started, in other words, whether or not the first performance pattern in the pattern sequence has been started, is ascertained (S31). If the result that has been ascertained by the processing of S31 is that the pattern sequence has been started (S31: yes), the buffer that has been furnished in the RAM 16, which is not shown in the drawing, is cleared (S32), the tick value is made "0" (S33), and the routine moves to the performance processing (S42) that will be discussed later.

[0056] Here, an explanation will be given regarding the performance processing (S42) while referring to the flowchart of Figure 8. The performance processing (S42) is processing for outputting the current performance data in accordance with the pattern number of the performance pattern and the tick value during the current performance to the sound source and, executes the deletion of an event or inserts an event in the current performance data based on the change in the case in which there has been a change in the current performance data due to the operation of a specified operator.

[0057] In the performance processing (S42), whether or not there are

performance data for the current tick in the current performance pattern number is ascertained (S51). If the result that is ascertained by the S51 processing is that performance data exist for the current tick in the current performance pattern number (S51: yes), whether or not the DEL switch 8 has been pressed is ascertained (S52). If the result that has been ascertained by the S52 processing is that the DEL switch has not been pressed (S52: no), the performance data for the current tick in the current performance pattern number (the current performance data) are output to the sound source section 22 (S53). After the processing of S53, the current performance data that have been output by the sound production instruction to the sound source section 22 in the S53 processing (the performance data for which the sound production instruction was made to the sound source section 22) are stored in the buffer together with the value of the current tick (S54). After the S54 processing, the routine moves to the processing of S57 which will be discussed later.

[0058] On the other hand, if the result that has been ascertained by the S52 processing is a case in which the DEL switch 8 has been pressed (S52: yes), whether or not a PAD switch 4 has been pressed together with the DEL switch 8 is ascertained (S55). If the result that has been ascertained by the S55 processing is that a PAD switch 4 has not been pressed (S55: no), since a musical tone that should be deleted (event) has not been selected, the current performance data are output to the sound source section 22 (S53) and the routine moves to the processing of S54.

[0059] On the other hand, if the result that has been ascertained by the S56 processing is that a PAD switch 4 has been pressed together with the DEL switch 8 (S55: yes), whether or not the PAD switch 4 (4a to 4h) that has been pressed and the current performance data correspond is ascertained (S56). If the result that has been ascertained by the S56 processing is that the PAD switch 4 that has been pressed and the current performance data do not correspond (S56: no), since performance data do not exist in the current performance data for which an

instruction that they should be deleted has been made by the PAD switch 4, the current performance data are output to the sound source 22 (S53) and the routine moves to the processing of S54.

[0060] On the other hand, if the result that has been ascertained by the S56 processing is a case in which the PAD switch 4 that has been pressed and the current performance data correspond (S56: yes), the processing of S53 to S54 is skipped and the routine moves to the processing of S57 that will be discussed later. In other words, the performance data that are included in the current performance data and for which an instruction to delete has been made using the DEL switch 8 and the PAD switches 4 are not output to the sound source 22 nor are they stored in the buffer. Therefore, sounds are not produced for the performance data which it has been ascertained by the S56 processing are to be deleted and, in addition, by means of the processing of S38 or S44, the data that are instructed to be deleted for the time that corresponds to the timing that is instructed to be deleted are deleted and are rewritten as a new song.

[0061] On the other hand, in those cases where the result that has been ascertained by the S51 processing is that performance data do not exist for the current tick of the current performance pattern (S51: no), since no musical tone data exist that should be produced for the current tick of the current performance pattern, the processing of S52 to S56 is skipped and the routine moves to the processing of S57.

[0062] In the S57 processing, whether or not a PAD switch 4 (4a to 4h) has been pressed independently, in other words, whether or not a PAD switch 4 has been pressed without pressing the DEL switch 8 is ascertained (S57). If the result that has been ascertained by the S57 processing is that a PAD switch 4 has been pressed independently (S57: yes), an event is formed for the performance data of the musical tone and strength that correspond to the PAD switch 4 (4a to 4h) that has been pressed and the performance data are output to the sound source section 22 (S58). After the S58 processing, the performance data for which a sound

production instruction has been made to the sound source 22 in the S58 processing are stored in the buffer together with the value of the current tick (S59). After the S59 processing, this performance processing (S42) ends.

[0063] By means of the processing of S58 to S59, the performance data that correspond to the PAD switch 4 that has been operated independently during the pattern sequence performance are emitted together with the performance data that have been output to the sound source section 22 by the S53 processing, in other words, the musical tones that correspond to the performance data for the current tick of the current performance pattern (the current performance data). Or in those cases where performance data that are output to the sound source section 22 do not exist, in other words, in those cases where the processing of S53 has not been carried out, sound is emitted only for a musical tone in accordance with the event for the performance data that has been formed based on the operation of the PAD switch 4 that was ascertained by the S57 processing. In addition, the event for the performance data that has been newly formed by the processing of S58 is inserted at the time that corresponds to the timing for which the insertion of the musical tone has been instructed by the processing of S38 or S44 that will be discussed later and rewritten as a new song.

[0064] On the other hand, if the result that was ascertained by the S57 processing is that a PAD switch 4 has not been pressed independently (S57: no), since a musical tone that is inserted as performance data for the current tick of the current performance data has not been formed, the processing of S58 to S59 is skipped and this performance processing (S42) ends.

[0065] The explanation will be given returning to Figure 7. After the execution of the performance processing (S42), the timing interrupt processing terminates and returns.

[0066] On the other hand, in those cases where the result that has been ascertained in the S31 processing is that a pattern sequence has not started (S31: no), "1" is added to the tick value (t) and the readout advances to the next tick

(S34). After the processing of S34, whether or not the tick that has been advanced to next is the end of the current performance pattern is ascertained (S35). If the next tick is not the end of the performance pattern (S35: no), the routine moves to the processing of the performance processing discussed above (S42).

[0067] In addition, if the result that has been ascertained by the S35 processing is that it is the end of the current performance pattern (S35: yes) , then whether or not it is the end of the pattern sequence is ascertained next (S36). If the result that is ascertained by the S36 processing is that it is not the end of the pattern sequence (S36: no), whether or not there has been a change in the performance processing (S42) to the performance data for the current tick of the current performance data is ascertained (S37).

[0068] Here, "there is a change to the performance data" indicates the following two cases in the performance processing (S42): (1) a case in which the processing of S53 to S54 is skipped based on the fact that the DEL switch 8 has been operated together with a PAD switch 4 during the pattern sequence performance, in other words, a case in which the event for the current tick of the current performance data is deleted, and (2) a case in which the event for the current tick of the current performance data is inserted based on the fact that a PAD switch 4 has been pressed independently during the pattern sequence performance.

[0069] In the case where the result that has been ascertained in the S37 processing is that there are changes in the performance data (S37: yes), the pattern sequence is rewritten based on the contents that have been stored in the buffer in the S54 processing (S38). In the S38 processing, first, the contents that have been stored in the buffer are copied to the region in the RAM 16 that is indicated by the specified address, a new pattern number (identifier) is assigned to the contents that have been copied, and a new performance pattern is produced. Incidentally, the new pattern number is a pattern number that maintains the attribution of the current (the original) pattern number (identifier). Next, the current

(the original) pattern number for the pattern sequence is rewritten with a new pattern number.

[0070] After the processing of S38, the buffer is cleared (S39), the tick value is made "0" (S40), the performance pattern is updated to the next pattern number (S41), and the routine moves to the performance processing (S42). On the other hand, if the result that has been ascertained by the S37 processing is that there has been no change in the performance data (S37: no), S38 is skipped and the routine moves to the processing of S39.

[0071] In addition, in a case where the result that has been ascertained by the S36 processing is that it is the end of the pattern sequence (S36: yes), whether or not there has been a change in the performance data in the performance processing (S42) is ascertained (S43). In a case where the result that has been ascertained by the S43 processing is that there has been a change in the performance data (S43: yes), the pattern sequence is rewritten in essentially the same manner as in the processing of S37 based on the contents that have been stored in the buffer in the S54 processing (S44), the automatic performance terminates automatically (S45) and the timing interrupt processing terminates and returns. On the other hand, if the result that has been ascertained by the S43 processing is that there has been no change in the performance data (S43: no), the processing of S44 is skipped and the routine moves to the S45 processing

[0072] Incidentally, when it is ascertained whether or not there has been a change in the performance data during the performance in the timer interrupt processing of the second preferred embodiment described above (Figure 7), the processing of rewriting the pattern sequence based on the contents that have been stored in the buffer in those cases where there has been a change in the performance data is carried out after the end of the pattern sequence has been ascertained by the processing of S36. However, it may also be configured such that this processing (S37 to 38 processing and S43 to S44) is done between S35 and S36.

[0073] Next, an explanation will be given regarding a third preferred embodiment while referring to Figure 9. Figure 9(a) shows the pattern sequence data 200 before the change and Figure 9(b) shows the pattern sequence data 210 after a partial change. Incidentally, the same codes are appended for the portions that are identical to those in the preferred embodiment 1 and their explanation has been omitted.

[0074] In the first preferred embodiment described above, it was set up such that a sequential performance is carried out in accordance with the performance order of the performance pattern. In contrast to this, in the third preferred embodiment, performance patterns of the same group (style) that are consecutive in the pattern sequence are repeated and performed as one block and when an operation has been made with an operator such as a pedal switch and the like, the shift to the next block is done after the performance to the end of that block.

[0075] An explanation will be given referring to Figure 9(a). For example, in the third preferred embodiment, the three PTN (pattern numbers) 10-1 (201a to 201c) and PTN 10-2 (201d) are viewed as the single block 201 and PTN 11-1 (202a) and PTN 11-2 (202b) as the single block 202. In this case, 201a to 201d of block 201 are repeated and performed and when a pedal switch (not shown in the drawing) that is contained in the operator group is stepped on, after the performance is done up to PTN 10-2 (201d), which is the end of the block 201, the next block 202 is performed. Here, in the same manner as in the first preferred embodiment, when a PAD switch 4 and, as required, the DEL switch 8 are operated, as is shown in Figure 9(b), a new pattern sequence 210 is produced that contains a newly formed performance pattern (PTN (10-2)' (211d)). When this new pattern sequence 210 is performed, PTN (pattern number) 10-1 (211a to 211c) and PTN (10-2)' (211d) are viewed as the single block 211 and repeated and performed until an operation of the pedal switch is made. In other words, by means of the automatic performance system 1 of the present invention, since the change is made with the performance pattern maintaining the original group assignment,

under conditions such as those of the third preferred embodiment, the performance order is not affected even in those cases where an automatic performance is carried out.

[0076] Explanations of the present invention have been provided based on the above preferred embodiments, however, the present invention is not in any way limited to the preferred embodiments described above and it can easily be surmised that various modifications and variations are possible within a range that does not deviate from the tenor of the present invention.

[0077] For example, in the first through the third preferred embodiments described above, all were set up as automatic performance systems with which a rhythm performance is carried out but they may also be automatic performance systems that carry out a bass performance or a chord performance.

[0078] In addition, in the first through the third preferred embodiments described above, the performance data are edited in the song mode by the operation of an operator but it may also be set up such that a separate switch is provided and the editing is carried out only when editing has been instructed by that switch so as to prevent an operator being inadvertently touched or the like during a performance and the performance data erroneously rewritten.

[0079] By means of the automatic performance system of the present invention, it is possible to change a portion of the pattern sequence by the operation of an operator during a performance. This provides the advantageous result of changing a portion of the performance by merely operating an operator during the performance without the need, as in the past, to redo from the beginning the creation of the performance pattern that it is desired to change in order to change a portion of the pattern sequence. In addition, since this kind of partial change of the pattern sequence is carried out while listening to the performance, the present invention also has the advantageous result that a musically suitable change is done easily while the performer perceives the flow of the entire performance.